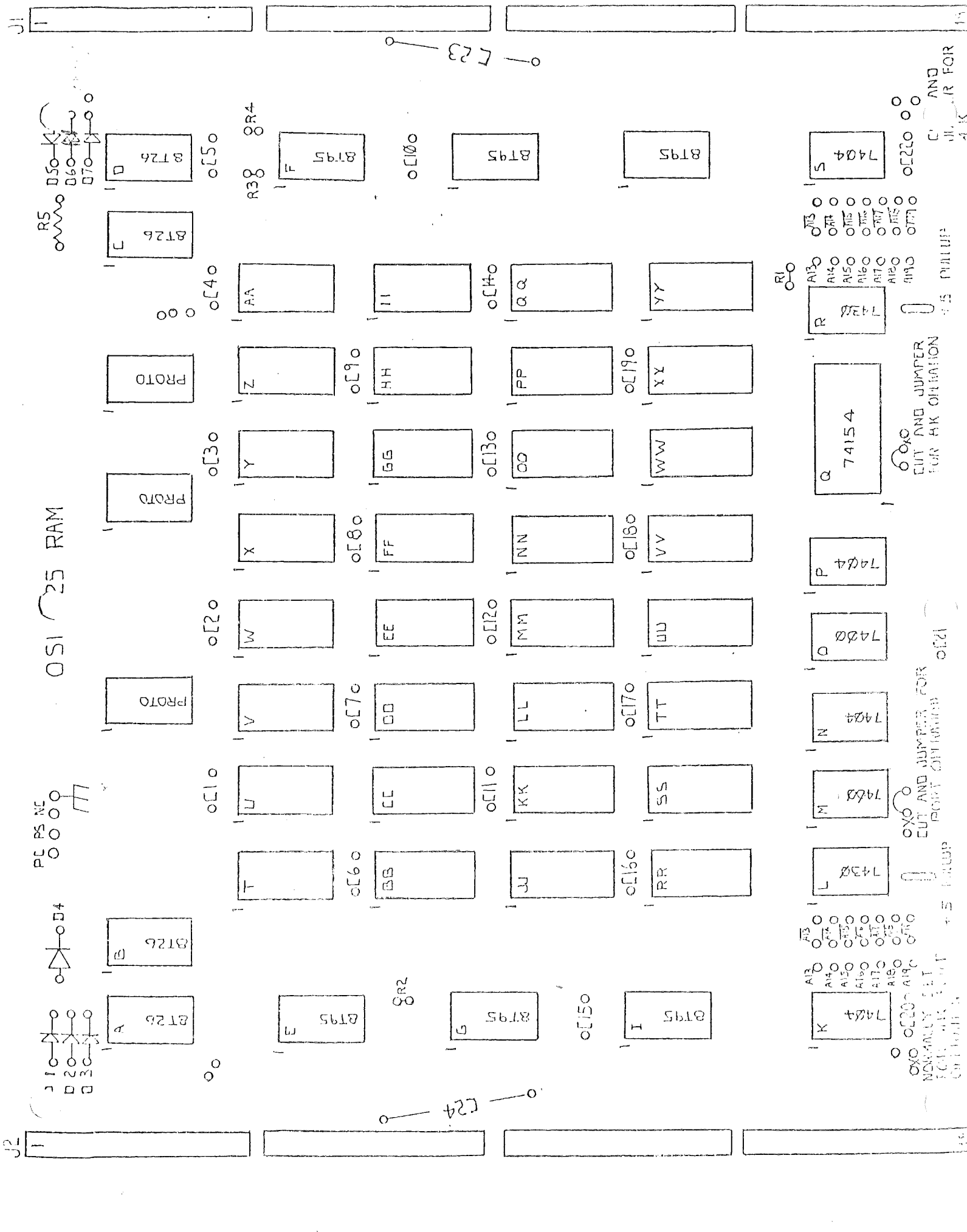


MODEL 525  
DUAL PORT STATIC RAM BOARD  
(2114 TYPE)

REV A	27 APR 1970



OS1 25 RAM

PC RS NC

PROTO

PROTO

PROTO

8126

8126

8195

8195

8195

7424

7424

7424

74154

7424

7424

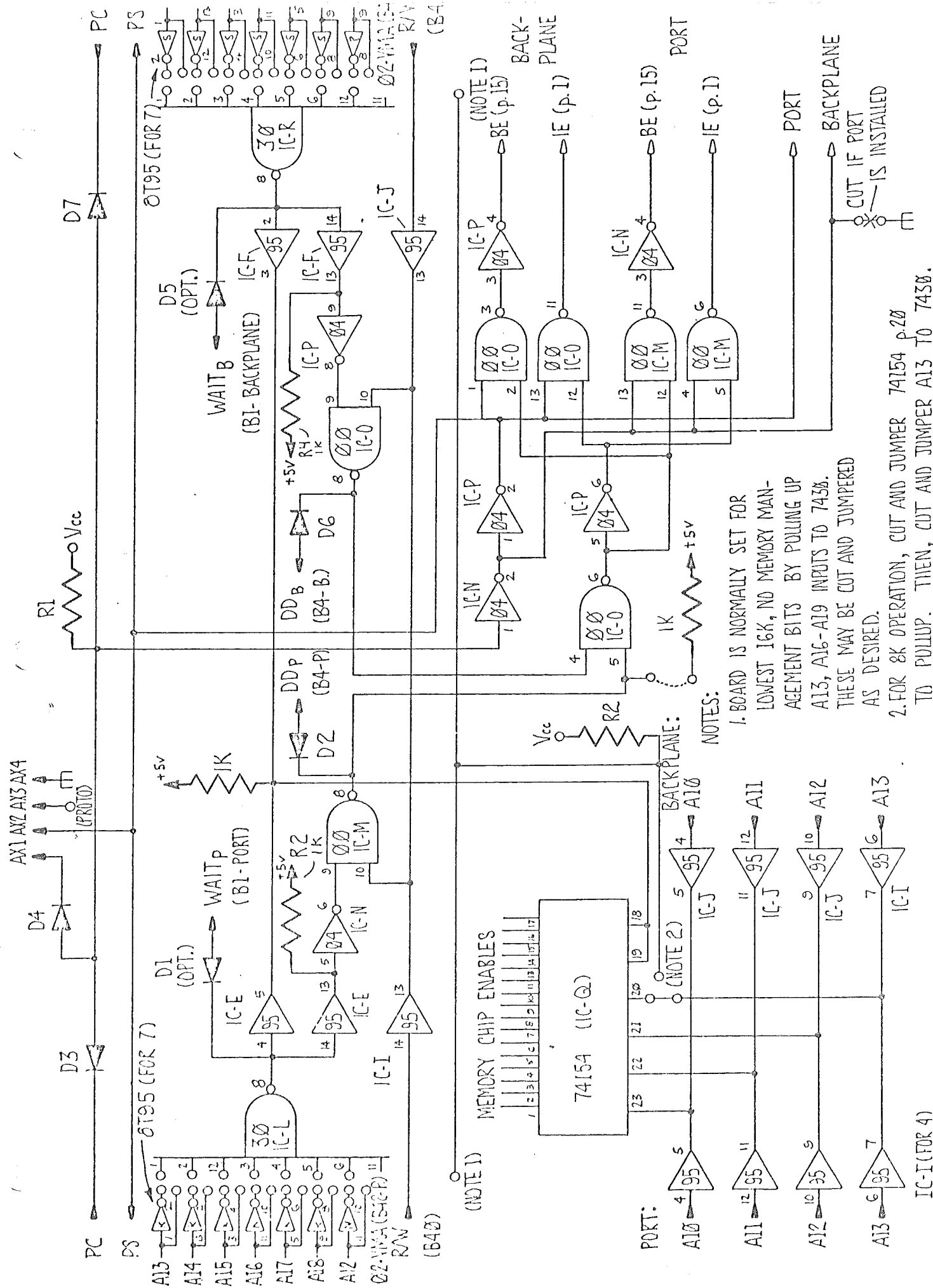
CUT AND JUMPER FOR AK OPERATION

CUT AND JUMPER FOR AK OPERATION

CUT AND JUMPER FOR PORT OPTION

CUT AND JUMPER FOR PORT OPTION

CUT AND JUMPER FOR PORT OPTION



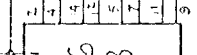
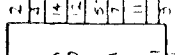
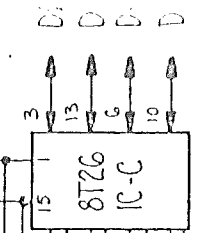
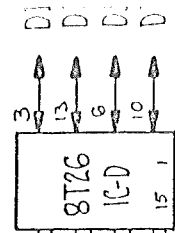
NOTES:  
 1. BOARD IS NORMALLY SET FOR LOWEST 16K, NO MEMORY MANAGEMENT BITS BY PULLING UP A13, A16-A19 INPUTS TO 7450. THESE MAY BE CUT AND JUMPERED AS DESIRED.  
 2. FOR 8K OPERATION, CUT AND JUMPER 74154 p.20 TO PULLUP. THEN, CUT AND JUMPER A13 TO 7450.

DIAGRAM 1 - ADDRESS DECODING AND PORT CONTROL CIRCUITRY

PORT:

D0  
D3  
D5  
D7  
D1  
D2  
D4  
D6

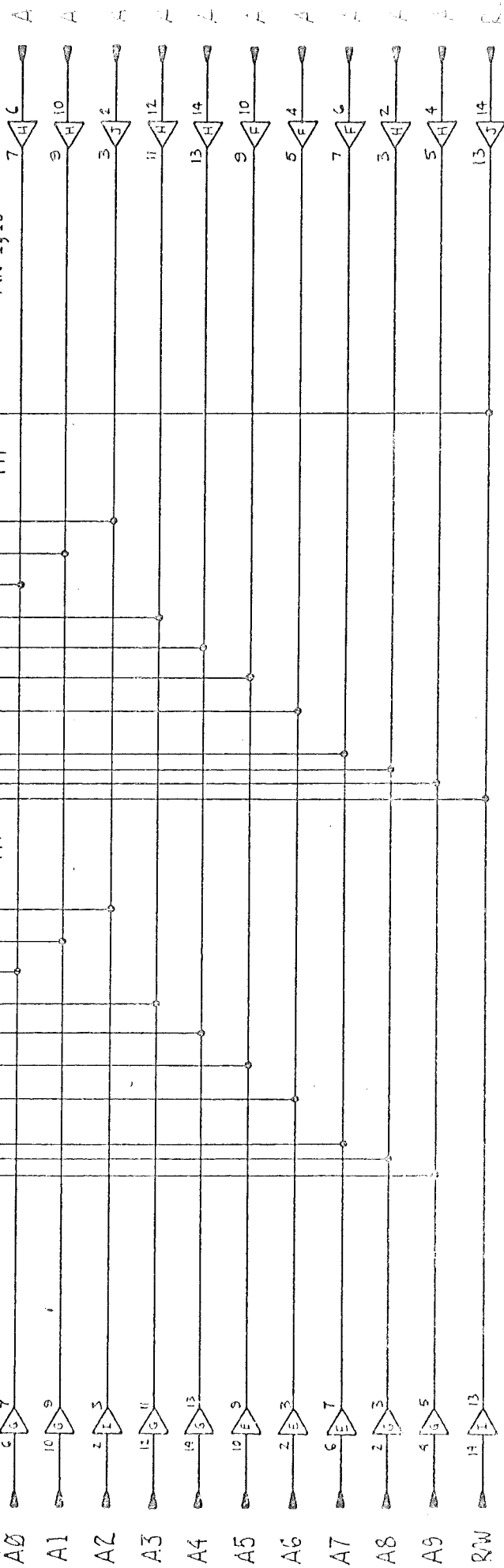
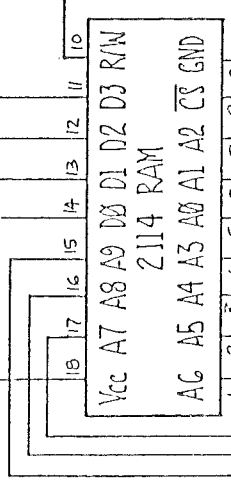
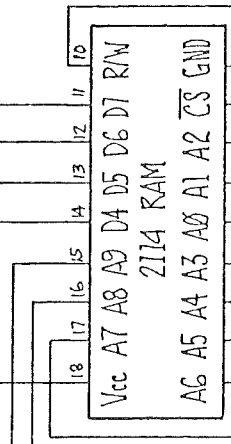
BACKPLANE



Vcc 0  
 PORT  
 8T95 ENABLE  
 IE BE  
 8T26 ENABLE

BACKPLANE  
 IE BE  
 8T95 ENABLE  
 8T26 ENABLE

TO IC-E,G,I  
 PIN 1,15  
 FROM 74154  
 'CHP SELECT'



8T95

8T95

DIAGRAM 2-10 IMPLEMENTATION

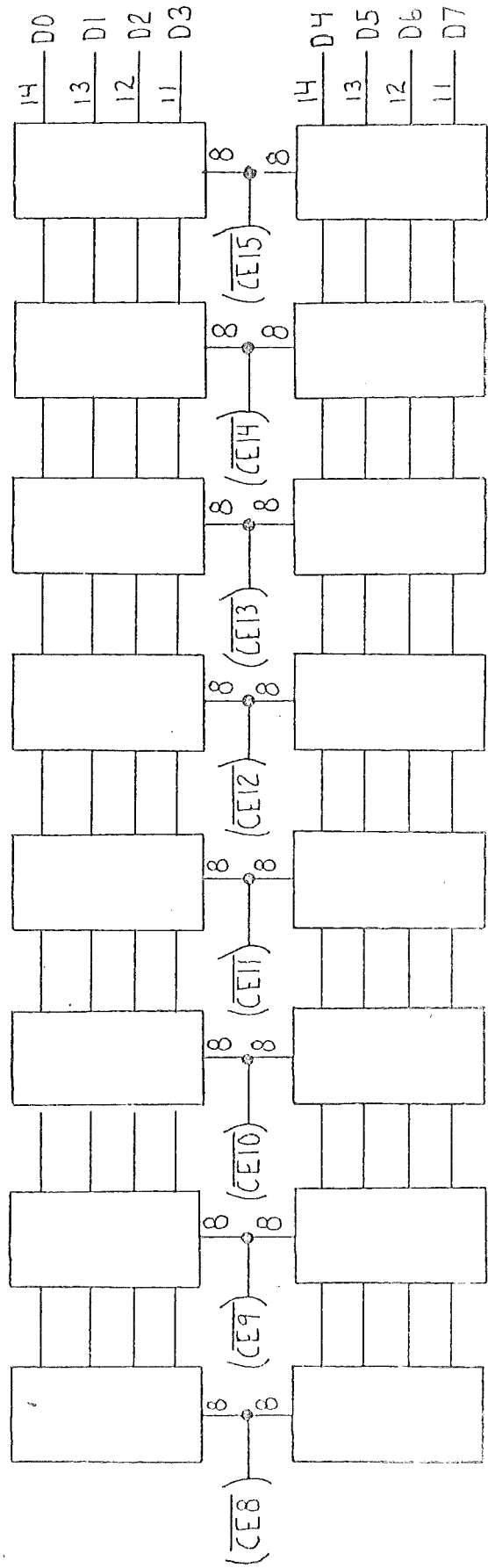
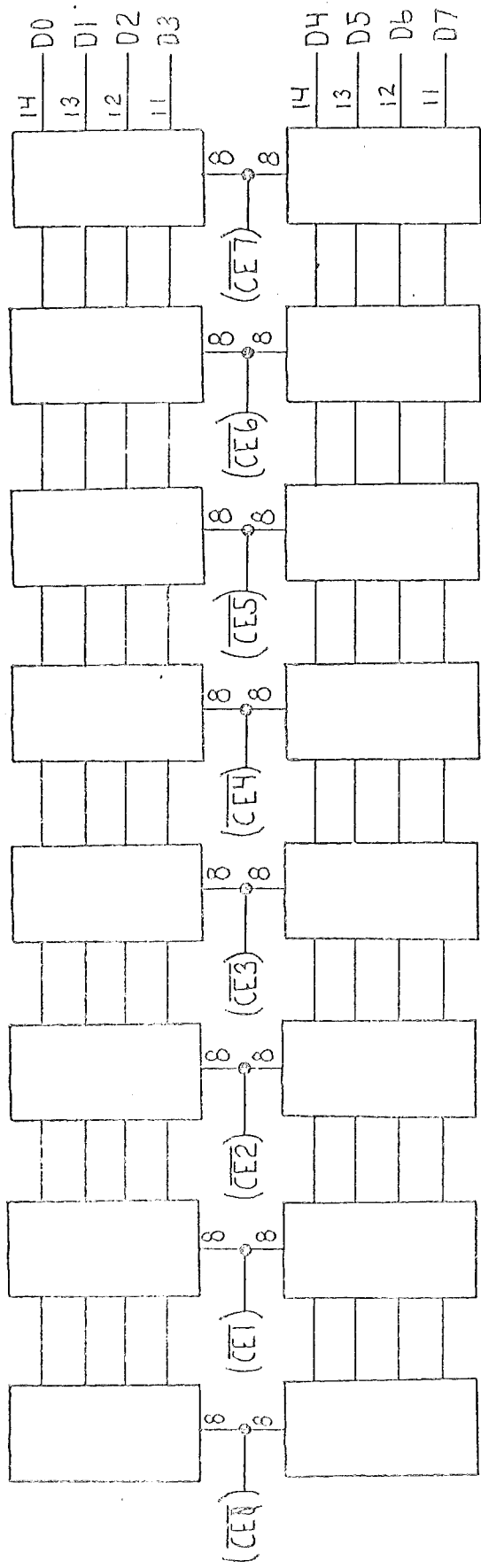


DIAGRAM 3 MEMORY ARRAY